HELIOS series For optical filters and applications on device wafers

Key benefits

Outstanding thickness distribution $(< \pm 0.5 \%)$

Absorption-free optical layers

Direct coating on:

- Structured and unstructured substrates
- Silicon and Glass Wafers
- Accurate thickness control
 - Layer stacks up to 1000 layers
 - Single-/ multilayers up to 10 μm
 - High precision even for thin layers > 5 nm

Low particle density

Dedicated production area



- Clean, highly professional assembly area for high end machines
- High quality assembly
- Clean area for machine testing and qualification
- Higher Quality and less errors of your product



Optics meets Semiconductor

Our industry-proven **LEYBOLD OPTICS HELIOS series** for fast, precise and fully-automated thin-film coatings is the ideal system for the challenging needs of the semiconductor industry.

Application examples

- Photonics
- Ambient light sensing
- Hyperspectral imaging
- Gesture and Facial recognition
- Color filter
- LiDAR sensor
- Wafer Level Optics









LEYBOLD OPTICS OMS 5100

- In-situ direct measurement of optical thickness
- Reproducible layer thickness ± 0.2 % run to run
- Precise rate calibration for very thin layers <5 nm
- No optimization runs needed at the machine



Technical data

System		HELIOS 800	HELIOS 1200
Technology		Plasma-assisted reactive magnetron -sputtering (PARMS/ PARMS+)	
Applications		MF sputtering (optional: RF, DC sputtering)	MF sputtering
Coating material		SiO2, Al2O3, Nb2O5, Ta2O5, HfO2, ZrO2, HfO2, Si3N4, ITO, Al, Ag, SiH	
Capacity		12* pcs. at Ø 200 mm / 8"	10 pcs. at Ø 300 mm/ 12" (including sub-rotation)
Process stations	Dual-magnetrons	Зх	4x**
	RF plasma sources	1x	1x
	Coating Ø (standard)	≤ 200 mm / ≤ 8"	≤ 300 mm / ≤ 12"
	(optional)	≤ 150 mm / ≤ 6"	≤ 200 mm / ≤ 8"
Layer monitoring	Time control	Yes	Yes
	Optical monitoring	LEYBOLD OPTICS OMS 5100, LEYBOLD OPTICS WB-OMS (1200)	
Dimensions	Width x length x height	7.3 m x 6.2 m x 3.0 m 288" x 242" x 118"	8.0 m x 4.5 m x 3.4 m 315" x 177" x 133"
Substrate handling	Manual Loading	 Single wafer loading Single cassette loading (up to 13 wafers) Multi cassette loading (up to 3x 13 wafers) 	Multi cassette loading (3x 13 wafers)
	Semiconductor-ready loading solutions	Wafer cassette loading (up to 25 wafers)SMIF integration	FOUP and EFEM integration

(*) One substrate less when optical monitoring is used (**) Mix of rotatable/planar cathodes customizable

